VNR VIGNANA JYOTHI INSTITUTE OF ENGINEERING AND TECHNOLOGY

Vignana Jyothi Nagar, Pragathi Nagar, Nizampet (S.O), Hyderabad – 500 090, Telangana, India

TIMETABLE FOR II B.TECH, I SEMESTER (R18), SUPPLEMENTARY EXAMINATION – MAY, 2024

Branch	4th May 2024 10:00 A.M. to 1:00 P.M. Saturday	7th May 2024 10:00 A.M. to 1:00 P.M. Tuesday	9th May 2024 10:00 A.M. to 1:00 P.M. Thursday	13th May 2024 10:00 A.M. to 1:00 P.M. Monday	15th May 2024 10:00 A.M. to 1:00 P.M. Wednesday	17th May 2024 10:00 A.M. to 1:00 P.M. Friday
CE	Probability, Statistics and Time Series (18BS1MT08)	Strength of Materials – I (18PC1CE01)	Fluid Mechanics (18PC1CE02)	Building Materials, Construction and Planning (18PC1CE03)	Surveying and Geomatics (18PC1CE04)	Disaster Management (18PC1CE05)
EEE	Complex Analysis and Transforms (18BS1MT05)	Electrical Circuit Analysis (18PC1EE01)	Electromagnetic Fields (18PC1EE02)	Electronic Devices and Circuits (18PC1EC02) **[Common to EEE, ECE]**	Electrical Machines – I (18PC1EE03)	Fluid Mechanics and Hydraulic Machines (18PC1ME16)
ME	Partial Differential Equations and Numerical Methods (18BS1MT06) **[Common to ME, AE]**	Data Structures Through C (18PC1CS06) **[Common to ME, EIE, AE]**	Thermodynamics (18PC1ME01) **[Common to ME, AE]**	Fluid Mechanics and Machinery (18PC1ME02)	Basic Electrical and Electronics Engineering (18ES1EE02) **[Common to ME, AE]**	--
ECE	Complex Analysis and Special Functions (18BS1MT04) **[Common to ECE, EIE]**	Network Theory (18PC1EC01)	Data Structures (18PC1IT01) **[Common to ECE, CSE, IT]**	Electronic Devices and Circuits (18PC1EC02) **[Common to EEE, ECE]**	Signals and Systems (18PC1EC03)	--
CSE	Probability, Statistics and Queuing Theory (18BS1MT07) **[Common to CSE, IT]**	Digital Logic Design (18ES1CS02) **[Common to CSE, IT]**	Data Structures (18PC1IT01) **[Common to ECE, CSE, IT]**	Mathematical Foundations for Computer Science (18PC1CS01) **[Common to CSE, IT]**	Object Oriented Programming through C++ (18PC1CS02) **[Common to CSE, IT]**	Computer Organization (18PC1IT02) **[Common to CSE, IT]**
EIE	Complex Analysis and Special Functions (18BS1MT04) **[Common to ECE, EIE]**	Data Structures Through C (18PC1CS06) **[Common to ME, EIE, AE]**	Electronic Circuits – I (18PC1EI01)	Sensors and Signal Conditioning (18PC1EI02)	Electronic Measurements (18PC1EI03)	--

28/02/2024

Branch	4th May 2024 10:00 A.M. to 1:00 P.M. Saturday	7th May 2024 10:00 A.M. to 1:00 P.M. Tuesday	9th May 2024 10:00 A.M. to 1:00 P.M. Thursday	13th May 2024 10:00 A.M. to 1:00 P.M. Monday	15th May 2024 10:00 A.M. to 1:00 P.M. Wednesday	17th May 2024 10:00 A.M. to 1:00 P.M. Friday
IT	Probability, Statistics and Queuing Theory (18BS1MT07) [Common to CSE, IT]	Digital Logic Design (18ES1CS02) [Common to CSE, IT]	Data Structures (18PC1IT01) [Common to ECE, CSE, IT]	Mathematical Foundations for Computer Science (18PC1CS01) [Common to CSE, IT]	Object Oriented Programming through C++ (18PC1CS02) [Common to CSE, IT]	Computer Organization (18PC1IT02) [Common to CSE, IT]
AE	Partial Differential Equations and Numerical Methods (18BS1MT06) [Common to ME, AE]	Data Structures Through C (18PC1CS06) [Common to ME, EIE,AE]	Thermodynamics (18PC1ME01) [Common to ME, AE]	Automotive Chassis (18PC1AE01)	Basic Electrical and Electronics Engineering (18ES1EE02) [Common to ME, AE]	--

Controller of Examinations

28/02/2024

Copy to :All HOD's

Note : 1. ANY OMISSIONS OR CLASHES IN THIS TIME TABLE MAY PLEASE BE INFORMED TO THE EXAMINATION BRANCH IMMEDIATELY
2. EVEN IF GOVERNMENT DECLARES HOLIDAY ON ANY OF THE ABOVE DATES, THE EXAMINATIONS SHALL BE CONDUCTED AS PER SCHEDULE.